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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/037,479
Filing Date	October 24, 2001
First Named Inventor	Bethard, Roger
Group Art Unit	2183
Examiner Name	Unknown

Sheet 1 of 1

Attorney Docket No: 1376.689US1

US PATENT DOCUMENTS

Examiner Initials*	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
SL	US-5,255,384	10/19/1993	Sachs, H. G., et al.	711	207	09/26/1991
SL	US-6,182,195	01/30/2001	Laudon, J. P., et al.	711	141	07/28/1998

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
SL	EP-0766177A1	04/02/1997	Kaiser, J. M.	G06F	12/10	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
SL		TELLER, P. J., et al., "Locating Multiprocessor TLBs at Memory", <u>Proceedings of the Twenty-Seventh Hawaii International Conference on Wailea, Vol. 1, 1) IEEE Computer Society 2) System Sciences, Architecture, (01/04/1994), 554-563</u>	

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Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached